

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
BUR920030161US1

Re Application Of: Anand et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/707,067	19 Nov 2003	Lam, Tuan Thiei	024241	2816	1066

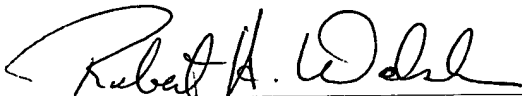
Invention: MODULAR DLL ARCHITECTURE FOR GENERATING MULTIPLE TIMINGS

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

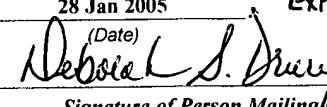
1 sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).


Signature

Robert A. Walsh Reg. No. 26,516
IBM
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Dated: 28 January 2005

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on	
28 Jan 2005 (Date)	EXPRESS MAIL # EV 02410153805
	
Signature of Person Mailing Correspondence	
Deborah S. Drury	
Typed or Printed Name of Person Mailing Correspondence	